



100

FIG. 1

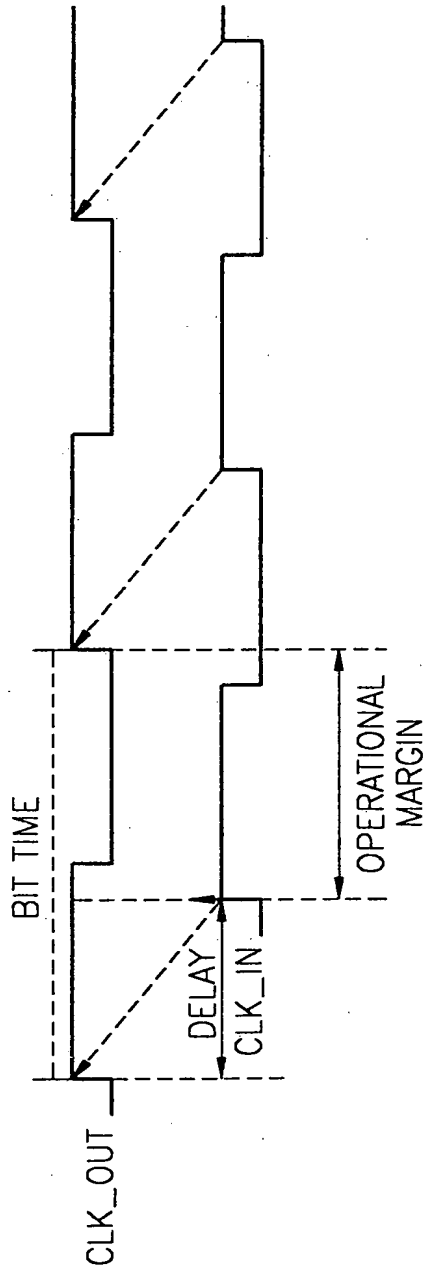
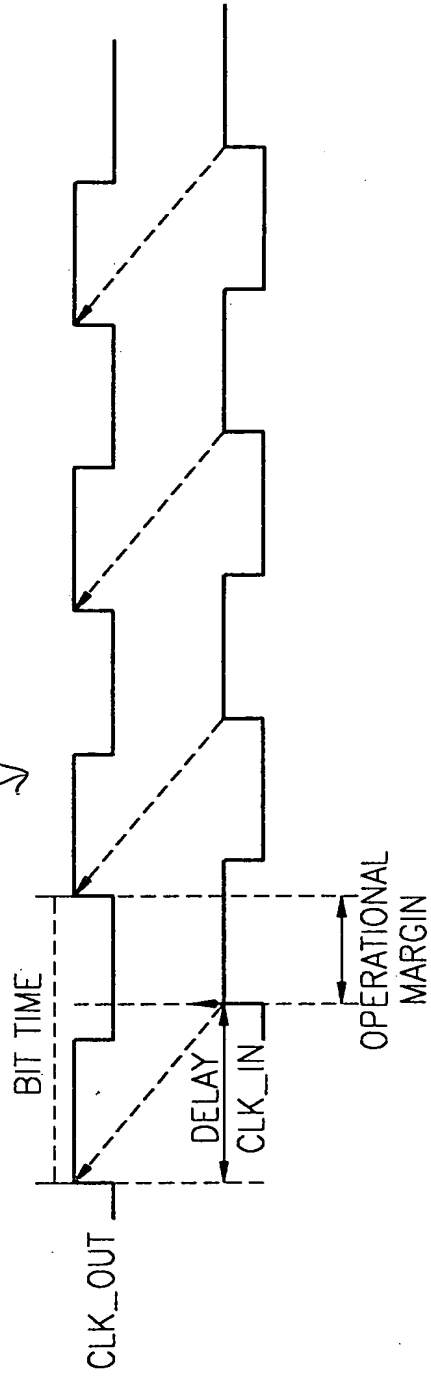


FIG. 2

200



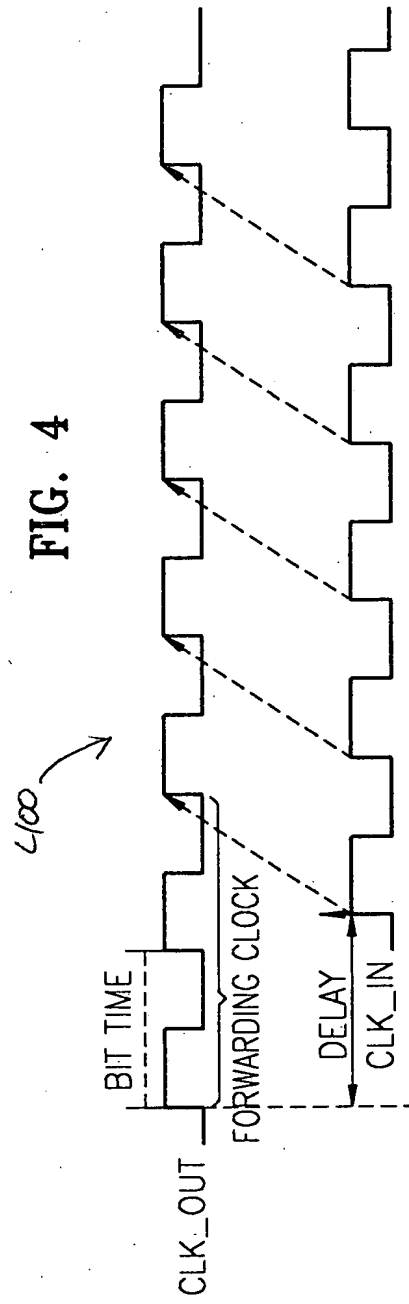
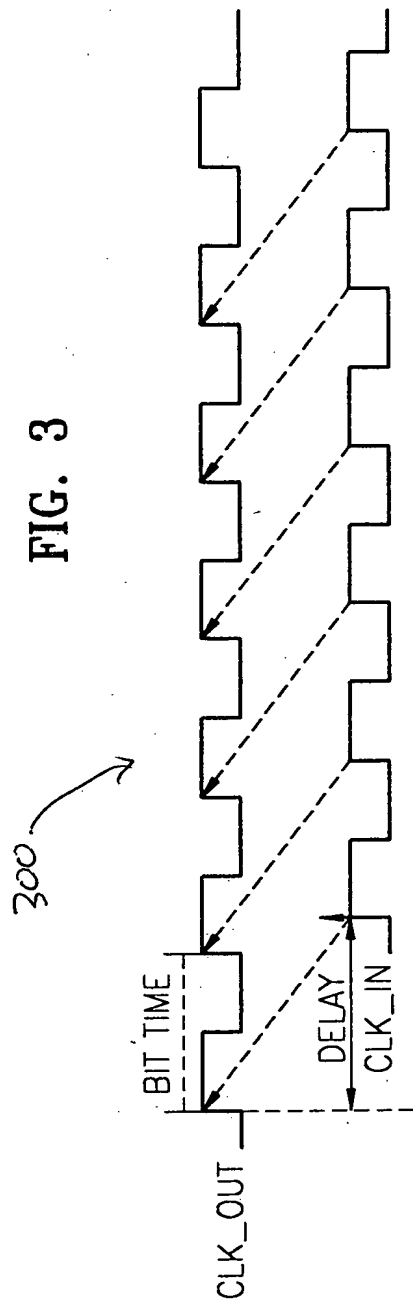


FIG. 5

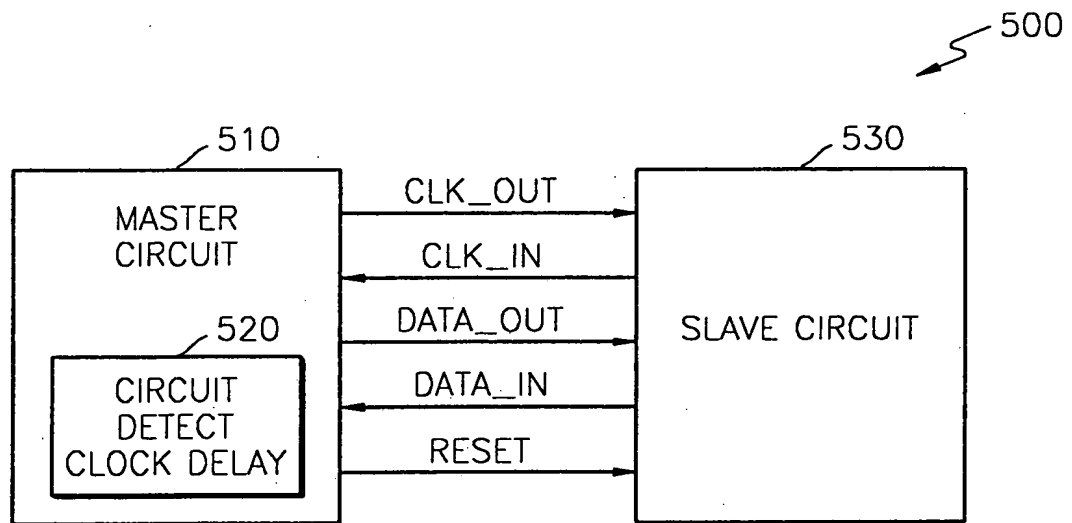


FIG. 6

520

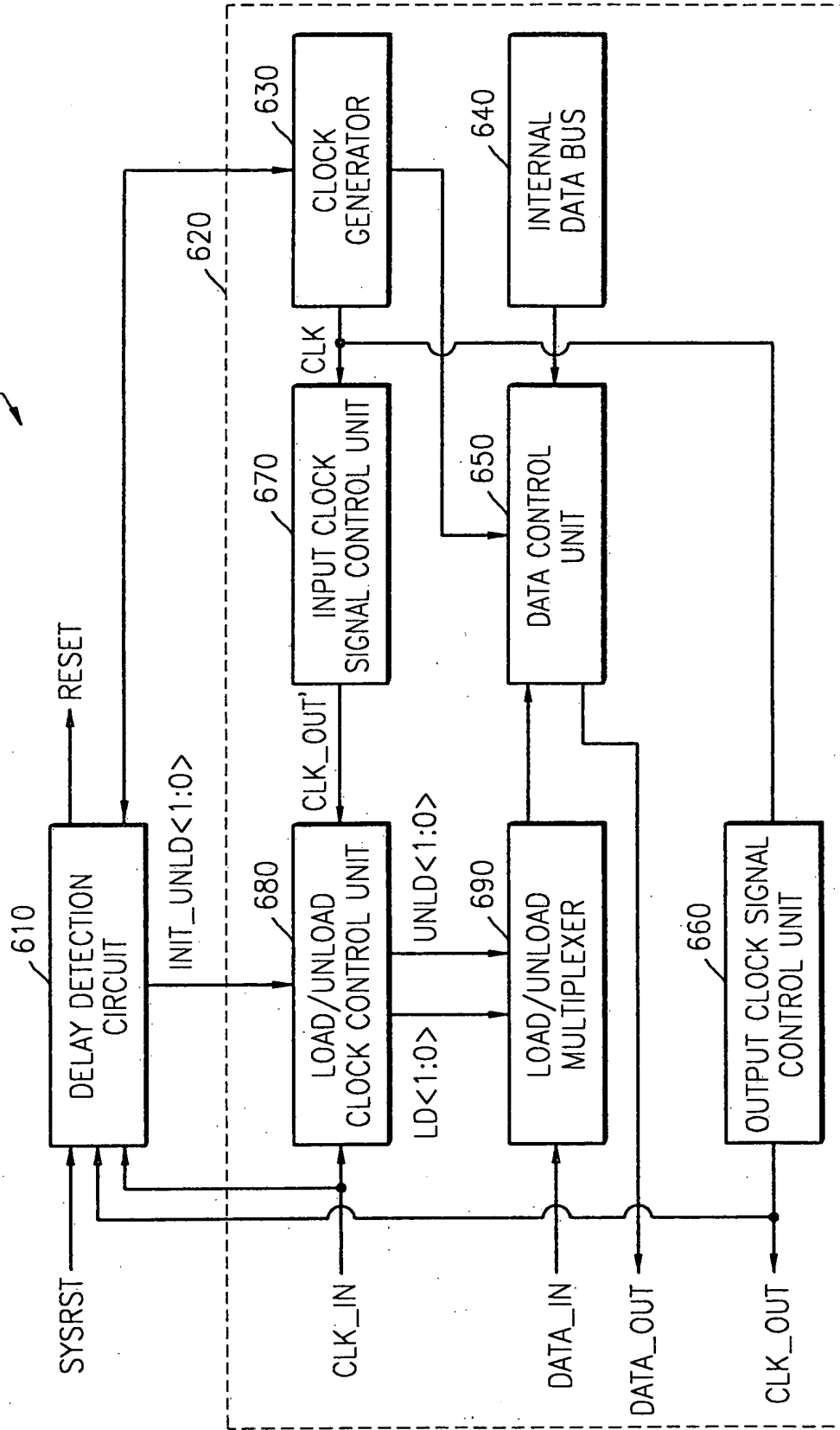


FIG. 8

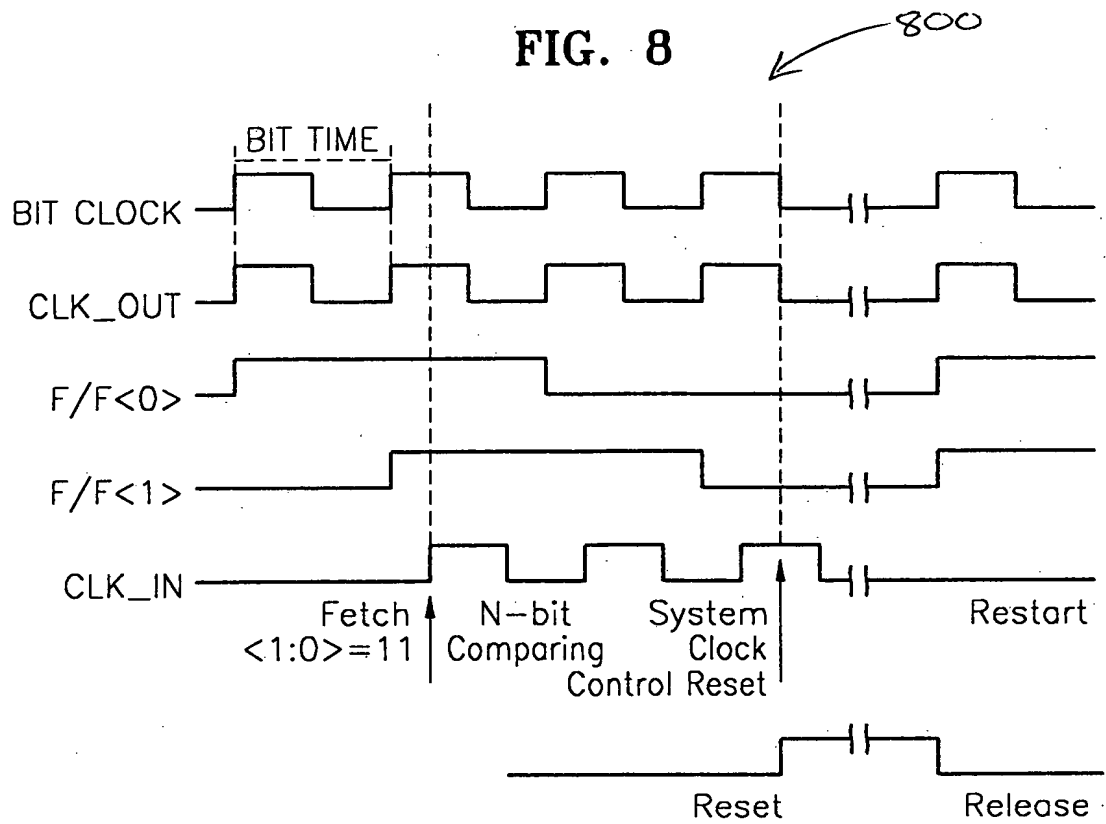


FIG. 9

900

MAXIMUM DELAY	INIT_UNLD<1:0>
MD<1BIT TIME	01
1<MD≤2BIT TIME	11
2<MD≤3BIT TIME	10
3<MD≤4BIT TIME	00

FIG. 10

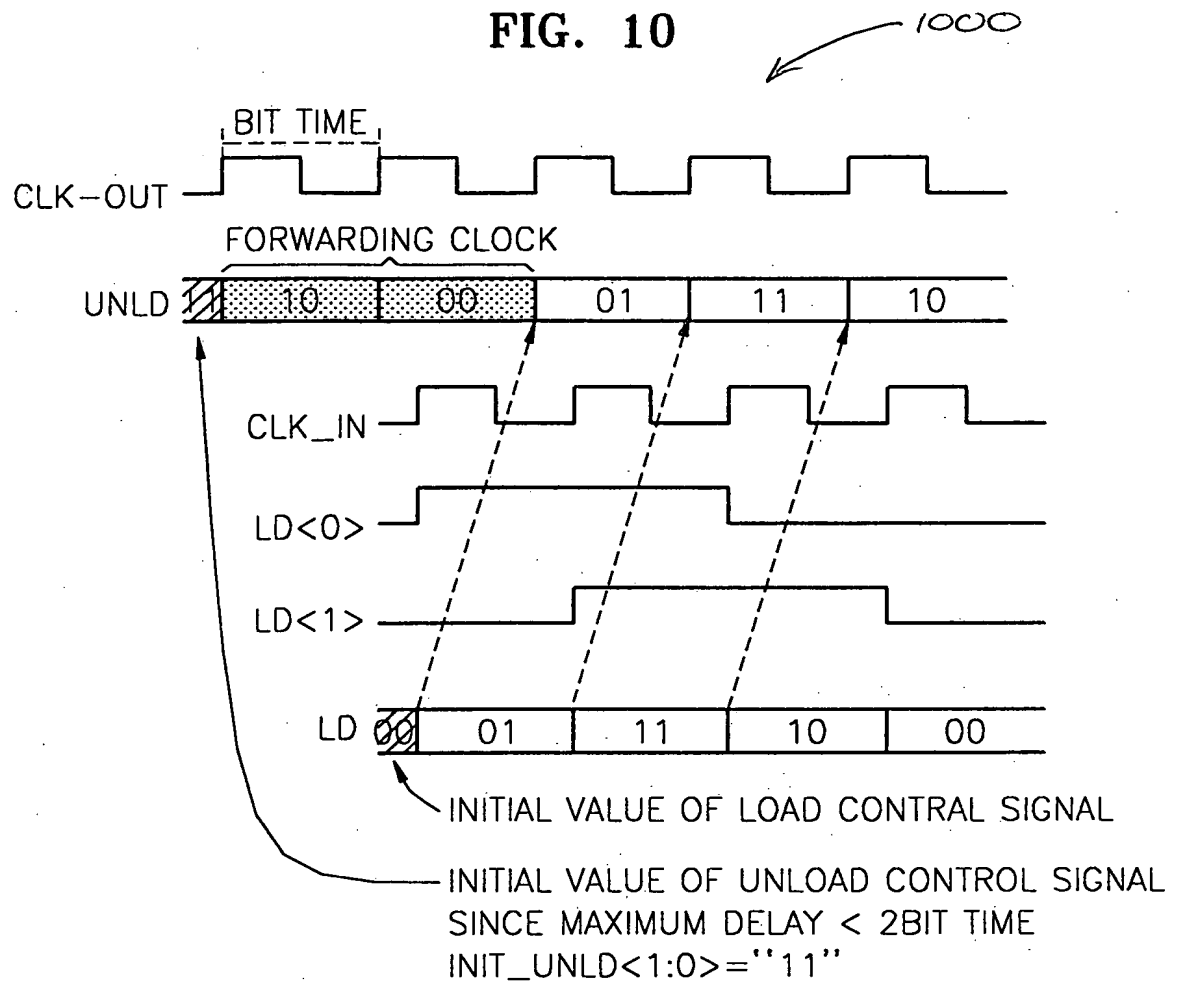


FIG. 11

